EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
S1	499	register with process$4 mode	US-PGPUB; USPAT	ADJ	ON	2008/01/17 18:41
S2	10613	(encrypt$4 or encod$4) near2 address$3	US-PGPUB; USPAT	ADJ	ON	2008/01/17 14:33
S3	0	S1 with S2	US-PGPUB; USPAT	ADJ	ON	2008/01/17 14:33
S4	0	S1 same S2	US-PGPUB; USPAT	ADJ	ON	2008/01/17 14:33
S5	0	register with process$4 mode with (encrypt$4 or encod$4) near2 address$3	US-PGPUB; USPAT	ADJ	ON	2008/01/17 14:33
S6	1	S2 with process$4 mode	US-PGPUB; USPAT	ADJ	ON	2008/01/17 14:34
S7	2	S2 same process$4 mode	US-PGPUB; USPAT	ADJ	ON	2008/01/17 14:34
S8	2	((encrypt$4 or encod$4) near2 address$3) same process$4 mode	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2008/01/17 14:37
S9	234	((encrypt$4 or encod$4) near2 address$3) near8 mode	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2008/01/17 14:37
S10	685	((encrypt$4 or encod$4) near2 address$3) near8 register	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2008/01/17 14:37
S11	40	S9 and S10	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2008/01/17 15:04

EAST Search History

S12	2	"5701493".pn.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2008/01/17 15:04
S13	1	"7024544".pn.	US-PGPUB; USPAT	ADJ	ON	2008/01/17 19:07
S14	2	shelor-charles.in.	US-PGPUB; USPAT	ADJ	ON	2008/01/17 15:06
S15	17258	register and address and mode and encoder	US-PGPUB; USPAT	ADJ	ON	2008/01/17 18:41
S16	427	register same address same mode same encoder	US-PGPUB; USPAT	ADJ	ON	2008/01/17 18:42
S17	14	register same address same mode same address encoder	US-PGPUB; USPAT	ADJ	ON	2008/01/17 19:08
S18	0	address same process$3 mode same address encoder	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2008/01/17 18:55
S19	23	register same address same mode same address encoder	USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2008/01/17 18:56
S20	825	711/220.ccls.	US-PGPUB; USPAT	ADJ	ON	2008/01/17 19:07
S21	181	register same address same mode and S20	US-PGPUB; USPAT	ADJ	ON	2008/01/17 19:17
S22	7	access$4 register same address same mode and S20	US-PGPUB; USPAT	ADJ	ON	2008/01/17 19:18
S23	22	register file same address same mode and S20	US-PGPUB; USPAT	ADJ	ON	2008/01/17 19:23
S24	0	(711/202,220.ccls or 71/43,218,229. ccls.) and (register file and address and mode and encod$4).clm.	US-PGPUB	ADJ	ON	2008/01/17 19:23
S25	6	(register file and address and mode and encod$4).clm.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2008/01/17 19:23

EAST Search History

S26	6	(register file and address and mode and encod$4).ab.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2008/01/17 19:24

IEEE *Xplore*®
RELEASE 2.4

Home | Login | Logout | Access Information | Alerts | Purchase History | Cart |

Welcome United States Patent and Trademark Office

Search Session History

BROWSE   SEARCH   IEEE XPLORE GUIDE

Edit an existing query or compose a new query in the Search Query Display.

Thu, 17 Jan 2008, 9:04:29 PM EST
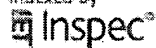
Search Query Display

Select a search number (#) to:

- Add a query to the Search Query Display
- Combine search queries using AND, OR, or NOT
- Delete a search
- Run a search

Recent Search Queries

#1   ((register <near/2> file <sentence> address <sentence> mode) <in>metadata)

#2   register <near/2> file <and> address <and> processing <near/2> mode

#3   ((register <near/2> file <paragraph> address <paragraph> mode)<IN>metadata)

Help   Contact Us   Privacy & :

© Copyright 2007 IEEE –

Indexed by

⬚ Inspec®

IEEE *Xplore* ®
RELEASE 2.4

□ **Search Result - Print Format**   < Back t

Key: **IEEE JNL** = IEEE Journal or Magazine, **IEE JNL** = IEE Journal or Magazine, **IEEE CNF** = IEEE Conference, II
CNF = IEE Conference, **IEEE STD** = IEEE Standard

1. **A new register file structure for the high-speed microprocessor**
   Kadota, H.; Ozawa, S.; Kawakami, K.; Ichinohe, E.;
   Solid-State Circuits, IEEE Journal of
   Volume 17, Issue 5, Oct 1982 Page(s):892 - 897

   IEEE JNL

2. **A 500 ps 32 × 8 register file implemented in GaAs/AlGaAs HBTs [F-RISC/G processor]**
   Nah, K.; Philhower, R.; Greub, H.; McDonald, J.F.;
   Gallium Arsenide Integrated Circuit (GaAs IC) Symposium, 1993. Technical Digest 1993., 15th Annual
   10-13 Oct. 1993 Page(s):71 - 74

   IEEE CNF

3. **Peripheral component interconnect (PCI) interface with the QuickLogic QL16×24B FPGA**
   Geber, C.; Yee, K.;
   WESCON/94. 'Idea/Microelectronics'. Conference Record
   27-29 Sept. 1994 Page(s):568 - 573

   IEEE CNF